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Semiconductor Device  
Modeling with Spice.

How to stimulate  
circuits faster and  
better with SPICE.

Table of Contents: PN-  
Junction Diode And  
Schottky Diode; Bipolar  
Junction Transistor  
(BJT); Junction Field-  
Effect Transistor (JFET);  
The MOS Transistor;

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BJT Parameter Measurements; MOS Parameter Measurements; Noise and Distortion; The SPICE Program; MESFET, ISFET, And Thyristor Devices; Appendix A: The Two-Terminal PN.

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The SPICE (Simulation Program, Integrated C

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ircuit Emphasis)  
electronic simulation  
program provides  
circuit elements and  
models for  
semiconductors. The  
SPICE element names  
begin with d, q, j, or m  
correspond to diode,  
BJT, JFET and MOSFET  
elements, respectively.

## **Semiconductor Devices in SPICE | Solid-state Device Theory ...**

Semiconductor Device  
*Page 6/26*

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Modeling With SPICE.

Giuseppe Massobrio.

Department of  
Electronics (DIBE)

University of Genova  
Genova, Italy. Paolo  
Antognetti.

Department of  
Electronics (DIBE)  
University of Genova  
Genova, Italy. Second  
Edition McGraw-Hill,  
Inc.

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With SPICE**

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Semiconductor device modeling with SPICE by Giuseppe Massobrio, 1993, McGraw-Hill edition, in English - 2nd ed.

## **Semiconductor device modeling with SPICE (1993 edition ...**

Semiconductor Device Modeling Giuseppe Massobrio, Paolo Antognetti SPICE (Software Program with Integrated Circuit



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Emphasis) is a powerful design aid that electronics engineers learn and is the world standard for circuit simulation. And when circuit designers are using the

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Model equations are essentially the same as semiconductor theoretical equations. In a device model, these parameter

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values are set. SPICE  
Device Models: Case  
Study of Diode Device  
Model. A device model  
of an actual diode is  
presented. This model  
is provided by ROHM.

## **SPICE Device Models: Diode Example--Part 1 |**

□□□□

Semiconductor device  
modeling creates  
models for the  
behavior of the  
electrical devices

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based on fundamental physics, such as the doping profiles of the devices. It may also include the creation of compact models (such as the well known SPICE transistor models), which try to capture the electrical behavior of such devices but do not generally derive them from the underlying physics.

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EE2.3 Semiconductor  
Modelling in SPICE /  
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matrix construction by  
inspection and lets us  
go directly from the  
netlist to the matrix  
equation.

## **EE 2.3: Semiconductor Modelling in SPICE Course homepage ...**

The basic models  
agree with the results

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of numerical two-dimensional device simulators. The real-device effects then augment the basic models. All the important real-device effects, such as shortchannel effects (SCEs), quantum mechanical confinement effects, mobility degradation, and parasitics are included in the models.

**BSIM - SPICE models**  
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## **enable FinFET and UTB IC designs ...**

commercial packages such as SPICE. 6 The relationship. ... physics and semiconductor device modeling, with strong emphasis on quantum transport. and Monte Carlo particle-based device simulations ...

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circuitry and processes of the device, the semiconductor vendor's proprietary information is protected. IBIS model behavior provides faster simulation time than structured models, e.g., SPICE models. IBIS File Structure IBIS version 1.1 introduced the baseline architecture suitable for most CMOS and TTL technologies.

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## **Simulating Altera Devices with IBIS Models**

Semiconductor device  
modeling with SPICE  
Giuseppe Massobrio,  
Paolo Antognetti  
Published in 1993 in  
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Modeling with SPICE.



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Giuseppe Massobrio,  
Paolo Antognetti.  
McGraw-Hill, 1993 -  
Technology &  
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on practicality of...

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Giuseppe Massabrio  
(Author), Paolo  
Antognetti (Author)

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Approach Many  
different SPICE  
versions and  
enhancements are  
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share “baseline”

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features and device models from original SPICE version (developed at UC Berkeley). However, baseline version SPICE JFET model DOES NOT include body bias effect. Baseline SPICE NMOS LEVEL 1 model (n-channel MOSFET, that includes ...

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## Semiconductor

...

SPICE (Software  
Program with  
Integrated Circuit

Emphasis) is a powerful design aid that electronics engineers learn and is the world standard for circuit simulation. And when circuit designers are using the various versions of SPICE to simulate circuits prior to fabrication and accurately predict future performance,

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this guide could be a useful reference.

## **Semiconductor device modeling with SPICE in SearchWorks ...**

He has done research in the areas of semiconductor device modeling as well as circuit simulation and optimization. He is the author of several papers on semiconductor device models for CAD

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applications. He is currently researching the development of semiconductor-based biosensor models for biomedical technologies.

## **Semiconductor Device Modeling: Giuseppe Massobrio, Paolo ...**

Semiconductor Device Modeling, McGraw-Hill Professional Publishing, 1998, ISBN

0-07-1349553 With all

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the clarity and hands-on practicality of the best-selling first edition, this revised version explains the ins and outs of SPICE, plus gives new data on modeling advanced devices such as MESFETs, ISFETs, and thyristors.

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Emphasis is placed on



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the practical applications of the advanced semiconductor technologies and the device level compact/spice modeling. This book is intended to provide reference information by selected, leading authorities in their domain of expertise. They are representing both academia and industry.

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