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The on board ADC (125 MS/s sampling frequency, 14-bit resolution) digitizes the RF signal from the antenna. The data coming from the ADC is processed by a in-phase/quadrature (I/Q) digital down-converter (DDC) running on the Red Pitaya's FPGA. The I/Q data is transmitted via TCP to the SDR programs such as SDR# and HSDR.

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Battery Fuse (Red LED) Shows that the Charging Station is
processing information. Processor Working Light Shows that the
Charging Station is processing information. 3 6 9 Pack
Temperature (Red LED) Battery pack charging temperature

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outside 0 - 45°C range. ToughPIX II Battery Pack Indicates that the on board battery pack fuse has blown.

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New Red Pitaya Beta OS v1.03 is released for all Red Pitaya products. Besides general improvements, we have implemented SCPI server support for SIGNALlab 250-12. The new OS can be downloaded for free.

LCR meter - Red Pitaya

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